

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Koichi Kawi et al.  
Serial No. : Continuation Application of 10/194,337  
For : ***SEMICONDUCTOR STORAGE DEVICE HAVING PAGE COPYING  
FUNCTION***  
Group Art Unit: Not Yet Assigned  
Examiner : Not Yet Assigned

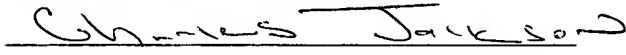
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Date of Deposit: October 31, 2003

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**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The Examiner's attention is respectfully drawn to the enclosed documents listed on the accompanying PTO form 1449.

As this Information Disclosure Statement is being filed before the mailing of the first Office Action, it is believed that no fee is required for entry of this paper. However, the

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Commissioner is hereby authorized to charge any such fee, or credit any overpayment to Deposit Account 50-0320.

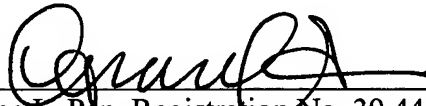
The filing of this Information Disclosure Statement is not an admission that the documents identified herein constitute prior art to the present application.

Applicants respectfully request that the Examiner considers and makes of record the documents cited herewith and that a copy of Form PTO-1449 be initialed by the Examiner and returned to the undersigned.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP  
Attorneys for Applicants

By: \_\_\_\_\_

  
Grace L. Pan, Registration No. 39,440  
Bruno Polito, Registration No. 38,580  
TEL: 212-588-0800  
FAX: 212-588-0500

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Based on Form PTO-1449 (3/90)  LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)				ATTY. DOCKET NO.		SERIAL NO.	
				790001-2020.1C		Not Yet Assigned	
				APPLICANT  Koichi Kawai et al.			
				FILING DATE		GROUP	
				October 31, 2003		Not Yet Assigned	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	6,023,423	2/8/00	Aritome			
	AB	5,822,245	10/1998	Gupta et al.	365	185.12	

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AC	9-180477	7/11/97	Japan				X

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
	AD	Nakamura et al., <i>A Novel Sense Amplifier for Flexible Voltage Operation NAND Flash Memories</i> , 1995 Symposium on VLSI Circuits Digest of Technical Papers, 1995, pp. 71-72.

			EXAMINER
			DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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